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**FACSIMILE COVER SHEET**

To: Examiner Theresa T. Doan

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Facsimile Number: 571-273-8300

Transmission Date: October 20, 2005

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Yang, *et al.*

Docket No.: TSM03-0929

Serial No: 10/823,158

Art Unit: 2814

Date Filed: April 13, 2004

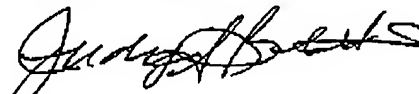
Title: Methods and Structures for Planar and Multiple-Gate Transistors Formed on SOI

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- Certification of Facsimile Transmission (1 page)
- Election (1 page)

Respectfully submitted,



Judy A. Betts  
Legal Assistant

Confirmation Respectfully Requested

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Mail Stop: Amendment  
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Alexandria, VA 22313-1450

Election

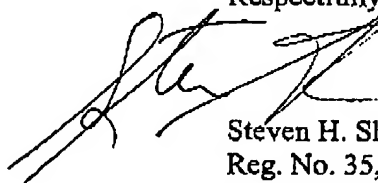
Dear Sir:

In response to Examiner's Restriction Requirement mailed September 22, 2005,  
Applicants respectfully elect Group II, Claims 1-18, without traverse.

Please contact Applicants' attorney with any questions regarding this matter.

Respectfully submitted,

October 20, 2005



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